

Notice of References Cited	Application/Control No. 10/797,771	Applicant(s)/Patent Under Reexamination EDIRISOORIYA ET AL.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	C	US-6,671,761	12-2003	Kim, Jin-soo	710/244
*	D	US-6,393,506	05-2002	Kenny, John D.	710/113
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"SAMBA-bus: a high performance bus architecture for system-on-chips" by Lu et al. (abstract only) Publication date: 9-13 Nov. 2003
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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